

Search Notes

Application/Control No.

10/728,870

Examiner

Peter Coughlan

Applicant(s)/Patent under
Reexamination

CHANG ET AL.

Art Unit

2129

SEARCHED

Class	Subclass	Date	Examiner
706	1	12/12/2005	PDC
706	15	12/12/2005	PDC
706	45	12/12/2005	PDC
700	1	12/12/2005	PDC
700	90	12/12/2005	PDC
370	400	12/12/2005	PDC
710	105	12/12/2005	PDC
704	236	12/12/2005	PDC
705	37	12/12/2005	PDC
709	224	12/12/2005	PDC

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
East-communication, duplex, eeprom, receive, network, neural network, computer, bus, CPU, memory, interface, operating system	12/12/2005	PDC
East-(cont)-monitor, self check, modify, update, parameter, configuration, tcp/ip, x25, async, sna, modular processor, board	12/12/2005	PDC
East(contl)-auto-balance, dual processors, share, divide, load, segment, split, parallel, backup, mutual backup, ethernet,	12/12/2005	PDC
East-(contll)-source mac, exchange, frame type,	12/12/2005	PDC
Inventor- Che-An Chang	12/12/2005	PDC
IEEE-duplex, eeprom, neural, bus, cpu, modular, update x24 tcp/ip, auto-balance, mutual backup, Che-An Chang	12/12/2005	PDC
370.400 with tcp/ip, x25, broadcast, device name, data format, 710.105-ful duplex, multi processor, ethernet, board, bus,	12/12/2005	PDC
704.236 with memory, interface, network, 705.37 with tcp/ip, x25, sna,	12/12/2005	PDC